CLAIMS:

1. A video circuit for processing video signals which show images on a display panel with linear light transition, comprising a gamma correction circuit, a quantizer and a sub-field generator circuit, characterized in that a coarse adjustment of the quantization is made in a first random-access memory and a fine adjustment of the quantization is made in a second random-access memory.

2. A video circuit for processing video signals which display images on a display panel with linear light transition, comprising a gamma correction circuit, a quantizer and a sub-field generation circuit, characterized in that most significant bits are quantized in a first random-access memory and least significant bits are quantized in a second random-access memory.

3. A video circuit for processing video signals which show images on a display panel with linear light transition, comprising a gamma correction circuit, a quantizer and a sub-field generation circuit, characterized in that a random-access memory replaces the quantizer.

4. A video circuit as claimed in claim 3, characterized in that the random-access memory replaces a dequantizer.

5. A video circuit as claimed in claim 3 and/or 4, characterized in that the random-access memory replaces a gamma correction circuit.

6. A video circuit as claimed in claim 4 and/or 5, characterized in that an inverse gamma circuit is arranged downstream of the dequantizer.

7. A video circuit as claimed in one or more of the preceding claims 3-6, characterized in that the random-access memory replaces a sub-field generator.

8. A video circuit as claimed in claim 7, characterized in that the sub-field generator applies values to a filter via a converter and a dequantizer.

9. A video circuit as claimed in claim 8, characterized in that the filter applies values to an adder which is situated in an input area of a second signal which represents pixel values of a neighboring line.

10. A video circuit as claimed in one or more of the preceding claims 7-9, characterized in that the sub-field generator applies values to the adder via a second converter and a second dequantizer.

11. A video circuit as claimed in claim 9 and/or 10, characterized in that pixel values of the neighboring line are quantized in a quantizer in a second random-access memory and in the second random-access memory sub-fields are generated in a sub-field generator.

12. A video circuit as claimed in claim 11, characterized in that the sub-field generator applies values to the quantizer of the second random-access memory.